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FCMT099N65S3

N-Channel SuperFET[®] III MOSFET 650 V, 30 A, 99 m Ω

Features

- 700 V @ T_J = 150 °C
- Typ. $R_{DS(on)} = 87 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_q = 56 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = TBD pF)
- · 100% Avalanche Tested
- RoHS Compliant

Applications

- · Server and Telecom Power Supplies
- · Solar Inverters
- Adaptors

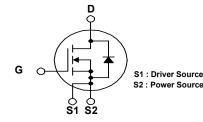
Description

SuperFET® III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET III MOSFET is very suitable for the switching power applications such as server/telecom power, adaptor and solar inverter applications.

The Power88 package is an ultra-slim surface-mount package (1 mm high) with a low profile and small footprint (8x8 mm²). SuperFET III MOSFET in a Power88 package offers excellent switching performance due to lower parasitic source inductance and separated power and drive sources. Power88 offers Moisture Sensitivity Level 1 (MSL 1).







Power88

Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

| Symbol | | Parameter | | FCMT099N65S3 | Unit |
|-----------------------------------|--|---------------------------------------|------------|--------------|------|
| V_{DSS} | Drain to Source Voltage | | | 650 | V |
| M | Cata ta Sauraa Valtaga | - DC | | ±30 | V |
| V_{GSS} | Gate to Source Voltage | - AC | (f > 1 Hz) | ±30 | V |
| | Drain Current | - Continuous (T _C = 25°C) | | 30 | А |
| ID | Drain Current | - Continuous (T _C = 100°C) | | 19 | A |
| I _{DM} | Drain Current | - Pulsed | (Note 1) | 75 | Α |
| E _{AS} | Single Pulsed Avalanche Energy | | (Note 2) | 145 | mJ |
| I _{AS} | Avalanche Current | | (Note 1) | 4.4 | Α |
| E _{AR} | Repetitive Avalanche Energy | | (Note 1) | 2.27 | mJ |
| dv/dt | MOSFET dv/dt | | 100 | Maa | |
| αν/αι | Peak Diode Recovery dv/dt | | (Note 3) | 20 | V/ns |
| D | Dawar Dissination | $(T_C = 25^{\circ}C)$ | | 227 | W |
| P_{D} | Power Dissipation - Derate Above 25°C | | 1.82 | W/°C | |
| T _J , T _{STG} | Operating and Storage Temperatu | re Range | | -55 to +150 | οС |
| T _L | Maximum Lead Temperature for S | soldering, 1/8" from Case for 5 Se | econds | 300 | οС |

Thermal Characteristics

| Symbol | Parameter | FCMT099N65S3 | Unit |
|-----------------|---|--------------|-------|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case, Max. | 0.55 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max. | 45 | 30/00 |

Package Marking and Ordering Information

| Part Number | Top Mark | Package | Reel Size | Tape Width | Quantity |
|--------------|--------------|---------|-----------|------------|----------|
| FCMT099N65S3 | FCMT099N65S3 | Power88 | 13" | 13.3 mm | 3000 |

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|---|--|---|------|------|------|------|
| Off Charae | cteristics | | | | | |
| D\/ | Drain to Source Breakdown Voltage | $V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$ | 650 | - | - | V |
| BV _{DSS} | | $V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 150^{\circ}\text{C}$ | 700 | - | - | V |
| ΔBV _{DSS} / ΔT _J | Breakdown Voltage Temperature Coefficient | I _D = 1 mA, Referenced to 25°C | - | 0.68 | - | V/°C |
| 1 | Zero Gate Voltage Drain Current | V _{DS} = 650 V, V _{GS} = 0 V | - | - | 10 | |
| I _{DSS} | | $V_{DS} = 520 \text{ V}, T_{C} = 125^{\circ}\text{C}$ | - | 2.77 | - | μΑ |
| I _{GSS} | Gate to Body Leakage Current | $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$ | - | - | ±100 | nA |

On Characteristics

| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS} = V_{DS}$, $I_D = 3 \text{ mA}$ | 2.5 | 1 | 4.5 | V |
|---------------------|--------------------------------------|---|-----|----|-----|----|
| R _{DS(on)} | Static Drain to Source On Resistance | V _{GS} = 10 V, I _D = 15 A | - | 87 | 99 | mΩ |
| 9 _{FS} | Forward Transconductance | V _{DS} = 20 V, I _D = 15 A | - | 17 | 1 | S |

Dynamic Characteristics

| C _{iss} | Input Capacitance | V _{DS} = 400 V, V _{GS} = 0 V, | - | 2270 | - | pF |
|------------------------|-----------------------------------|---|---|------|---|----|
| C _{oss} | Output Capacitance | f = 1 MHz | - | 50 | - | pF |
| C _{oss(eff.)} | Effective Output Capacitance | V _{DS} = 0 V to 400 V, V _{GS} = 0 V | - | 500 | - | pF |
| C _{oss(er.)} | Energy Related Output Capacitance | V _{DS} = 0 V to 400 V, V _{GS} = 0 V | - | 74 | - | pF |
| Q _{g(tot)} | Total Gate Charge at 10V | V _{DS} = 400 V, I _D = 15 A, | - | 56 | - | nC |
| Q_{gs} | Gate to Source Gate Charge | V _{GS} = 10 V | - | 13 | - | nC |
| Q_{gd} | Gate to Drain "Miller" Charge | (Note 4) | - | 23 | - | nC |
| ESR | Equivalent Series Resistance | f = 1 MHz | - | 0.5 | - | Ω |

Switching Characteristics

| $t_{d(on)}$ | Turn-On Delay Time | | | - | 22 | - | ns |
|---------------------|---------------------|---|---------|---|----|---|----|
| t _r | Turn-On Rise Time | $V_{DD} = 400 \text{ V}, I_D = 15 \text{ A},$ | | - | 20 | - | ns |
| t _{d(off)} | Turn-Off Delay Time | V_{GS} = 10 V, R_g = 4.7 Ω | | - | 58 | - | ns |
| t _f | Turn-Off Fall Time | 1) | Note 4) | - | 5 | - | ns |

Source-Drain Diode Characteristics

| I_S | Maximum Continuous Source to Drain Diode Forward Current | | - | - | 30 | Α |
|-----------------|--|--|---|-----|-----|----|
| I_{SM} | Maximum Pulsed Source to Drain Diode Forward Current | | | - | 75 | Α |
| V_{SD} | Drain to Source Diode Forward Voltage | V _{GS} = 0 V, I _{SD} = 15 A | - | - | 1.2 | V |
| t _{rr} | Reverse Recovery Time | V _{GS} = 0 V, I _{SD} = 15 A, | - | 352 | - | ns |
| Q _{rr} | Reverse Recovery Charge | $dI_F/dt = 100 A/\mu s$ | - | 6.5 | - | μC |

Notes:

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. I_{AS} = 4.4 A, R_G = 25 Ω , starting T_J = 25°C.
- 3. I $_{SD} \leq$ 15 A, di/dt \leq 200 A/µs, V $_{DD} \leq$ 400 V, starting T $_{J}$ = 25°C.
- ${\bf 4.} \ {\bf Essentially independent \ of \ operating \ temperature \ typical \ characteristics.}$

Typical Performance Characteristics

Figure 1. On-Region Characteristics

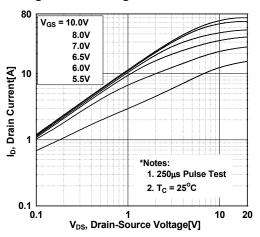


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

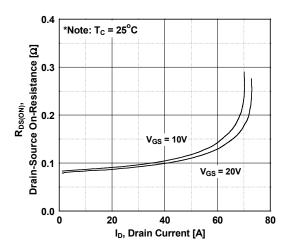


Figure 5. Capacitance Characteristics

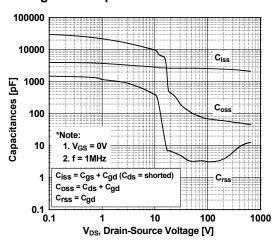


Figure 2. Transfer Characteristics

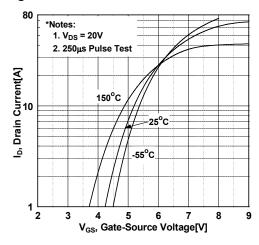


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

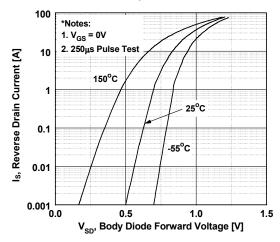
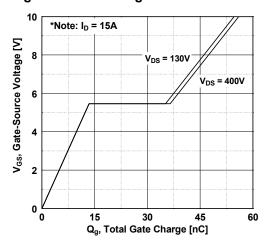


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

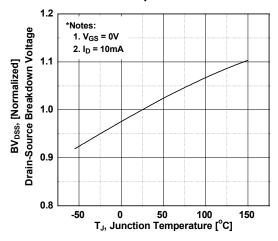


Figure 9. Maximum Safe Operating Area

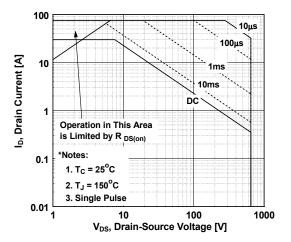


Figure 11. Eoss vs. Drain to Source Voltage

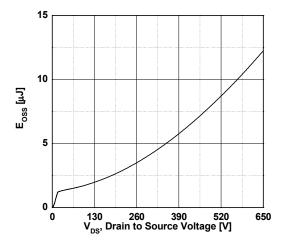


Figure 8. On-Resistance Variation vs. Temperature

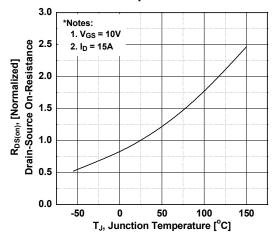
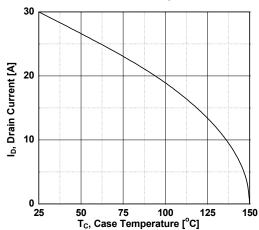
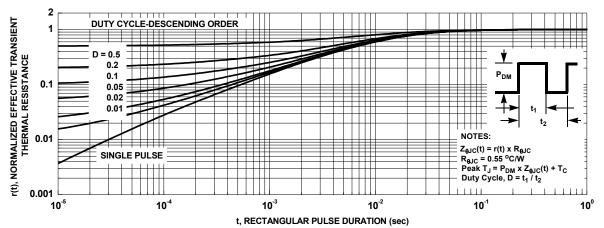


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)





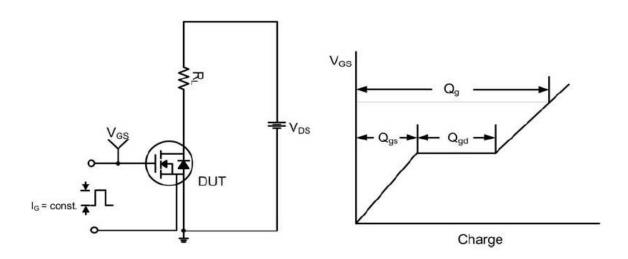


Figure 13. Gate Charge Test Circuit & Waveform

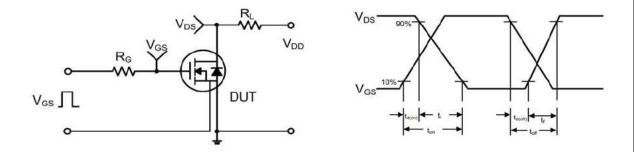


Figure 14. Resistive Switching Test Circuit & Waveforms

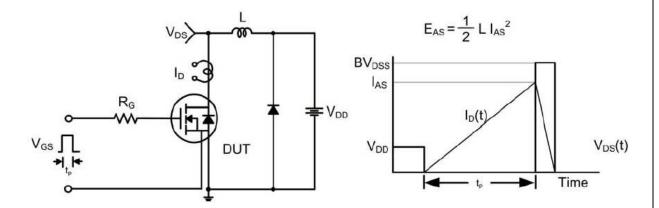
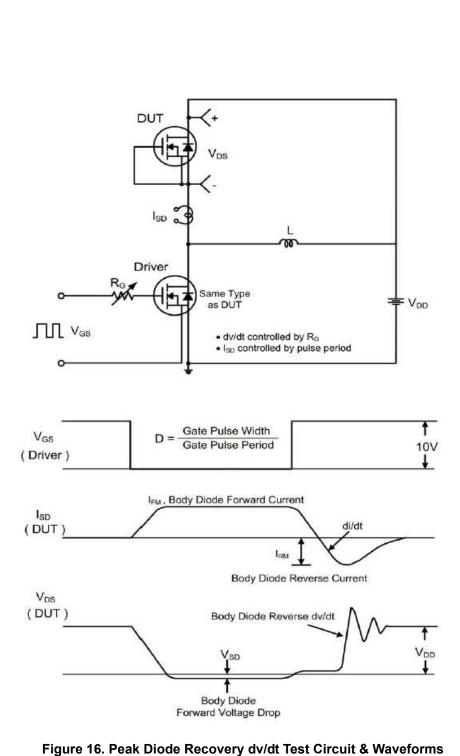
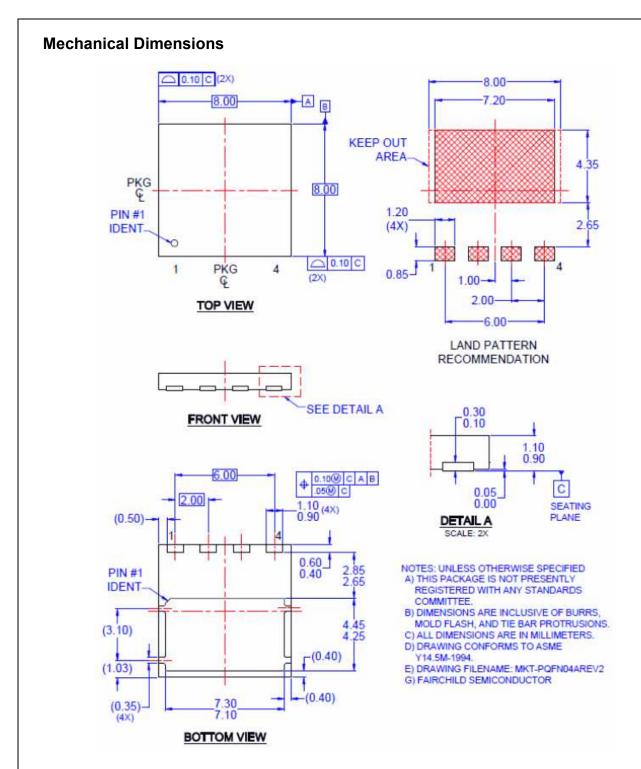


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms



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