

NPN Darlington Power Transistor

This Darlington transistor is a high voltage, high speed device for use in horizontal deflection circuits in TV's and CRT's.

• High Voltage:

$$V_{CEV} = 330 \text{ or } 400 \text{ V}$$

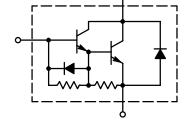
• Fast Switching Speed:

$$t_c = 1.0 \, \mu s \, (max)$$

• Low Saturation Voltage:

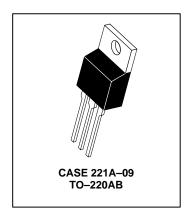
$$V_{CE(sat)} = 1.5 \text{ V (max)}$$

- Packaged in JEDEC TO-220AB
- Damper Diode V_F is specified.
 V_F = 2.0 V (max)



BU806

8.0 AMPERE DARLINGTON NPN POWER TRANSISTORS 60 WATTS 200 VOLTS



MAXIMUM RATINGS

Rating	Symbol	BU806	Unit
Collector–Emitter Voltage	V _{CEO}	200	Vdc
Collector–Emitter Voltage	V _{CEV}	400	Vdc
Collector-Base Voltage	V _{CBO}	400	Vdc
Emitter–Base Voltage	V _{EBO}	6.0	Vdc
Collector Current — Continuous — Peak	Ic	8.0 15	Adc
Emitter–Collector Diode Current	I _F	10	Adc
Base Current	I _B	2.0	Adc
Total Device Dissipation, $T_C = 25^{\circ}C$ Derate above $T_C = 25^{\circ}C$	P_{D}	60 0.48	Watts W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to 150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	2.08	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	70	°C/W
Lead Temperature for Soldering Purposes, 1/8" from Case for 5.0 Seconds	T _L	275	°C

BU806

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERI	STICS					
Collector–Emitter Sustaining Voltage (1) (I _C = 100 mAdc, I _B = 0)		V _{CEO(sus)}	200	_	_	Vdc
	ector Cutoff Current I_{CES} — V_{CE} = Rated V_{CBO} , V_{BE} = 0)				100	μAdc
Collector Cutoff Cu (V _{CE} = Rated V _C	off Current I_{CEV} — — I_{CEV}		_	100	μAdc	
Emitter Cutoff Current (V _{EB} = 6.0 Vdc, I _C = 0)		I _{EBO}	_	_	3.0	mAdc
ON CHARACTERIS	TICS (1)					
Collector–Emitter S (I _C = 5.0 Adc, I _B	S .	V _{CE(sat)} — —		1.5	Vdc	
Base–Emitter Satu (I _C = 5.0 Adc, I _B	•	V _{BE(sat)}	2.		2.4	Vdc
Emitter–Collector I (I _F = 4.0 Adc)			2.0	Vdc		
SWITCHING CHAR	ACTERISTICS					
Turn-On Time	(Resistive Load, V _{CC} = 100 Vdc,	t _{on}	_	0.35	_	μs
Storage Time	$I_C = 5.0 \text{ Adc}, I_{B1} = 50 \text{ mAdc},$	t _s	_	0.55	_	μs
Fall Time	$I_{B2} = 500 \text{ mAdc}$	t _f	_	0.20	_	μs
Crossover Time (I _C = 5.0 Adc, I _{B1} = 50 mAdc, V _{BE(off)} = 4.0 Vdc, V _{clamp} = 200 Vdc, L = 500 μ H)		t _c	_	0.40	1.0	μs

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1%.

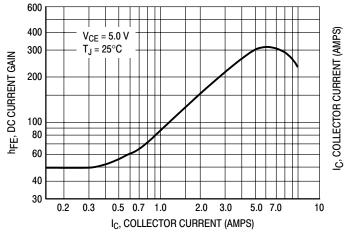


Figure 1. DC Current Gain

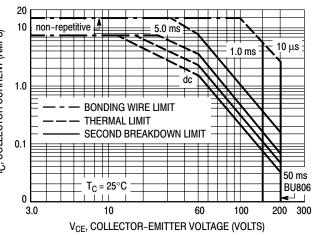
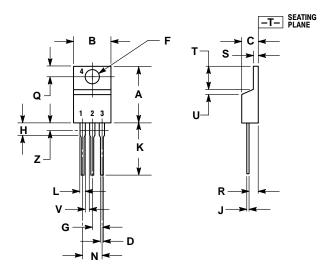


Figure 2. Safe Operating Area (FBSOA)

PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 ISSUE AA



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

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