

BT151S-650L

APPLICATIONS

Mesa glass passivation technology; Have high blocking voltage and high temperature stability cleaner; Electric tools such as motor speed controller; Solid state relay; Heating controller (temperature); Other phase control circuit Minimum Lot-to-Lot variations for robust device performance and reliable operation



ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	650	V
V _{RRM}	Repetitive peak reverse voltage	650	V
It(av)	On-state current Tc=80℃	7.5	А
I _{TSM}	Surge non-repetitive on-state current ,T=10ms	120	А
P _{G(AV)}	Average gate power	5	W
di/dt	Repetitive rate of rise of on-state current after triggering	5	A/us
l ² t	$I^{2}t$ for fusing t = 10 ms	72	A ² S
I _{GM}	Peak gate current tp=20us ,Tj=125℃	2	A
Tj	Operating Junction temperature	-40 ~+125	°C
T _{stg}	Storage temperature	-40 ~+150	°C

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INCHANGE SEMICONDUCTOR

isc Thyristors

BT151S-650L

ELECTRICAL CHARACTERISTICS (TC=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RRM} =650V, Тј=125℃			5	mA
I _{DRM}	Repetitive peak off-state current	V _{DRM} =650V, Тј=125℃			5	mA
V _{TM}	On-state voltage	I _{TM} = 23A			1.75	V
I _{GT}	Gate-trigger current	V_D =12V; R _L =100 Ω			5	mA
V _{GT}	Gate-trigger voltage	V_{D} =12V; R _L =100 Ω			1.5	V
Iн	Holding current	I⊤=0.5A			20	mA
dv/dt	Critical rate of rise of off-state voltage	V _D =0.67V _{DRM} Tj=125℃	200			V/us
R _{th(j-c)}	Thermal resistance junction to mounting base	in free air		1.75		°C/W



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