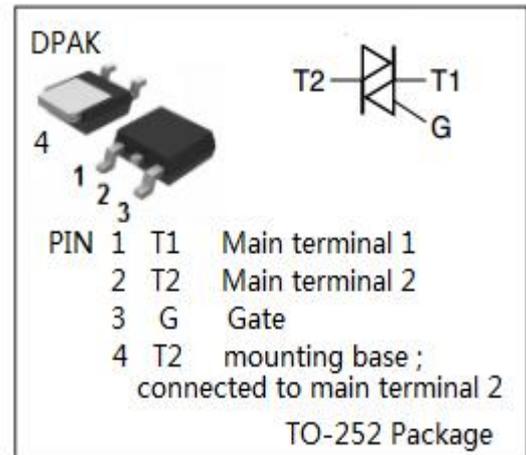


DESCRIPTION

- High blocking voltage capability
- Surface-mountable package
- Low holding current for low current loads and lowest EMI at commutation.
- Triggering in all four quadrants
- Very sensitive gate
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


FEATURES

- General purpose motor control
- General purpose switching

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	800	V
I _{T(RMS)}	RMS on-state current (full sine wave; T _{mb} ≤ 107°C)	4	A
I _{TSM}	Non-repetitive peak on-state current (T _j = 25°C; T _p = 20ms)	40	A
I ² t	I ² t for fusing t _p = 10ms; sine-wave pulse	8	A ² S
di/dt	T _j = 125°C	100	A/us
I _{GM}	Peak gate current	4	A
P _{G(AV)}	Average gate power dissipation	0.5	W
T _j	Operating junction temperature	-40~125	°C
T _{stg}	Storage temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT	
I _{GT}	Gate trigger current	V _D =12V; I _T = 0.1A, R _L = 100 Ω	I	10	mA
			II	10	
			III	10	
			IV	25	
V _{GT}	Gate trigger voltage		1.5	V	
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} , V _R =V _{RRM} , T _j =125°C	5 500	uA	
I _{DRM}	Repetitive peak off-state current	V _D =V _{DRM} , V _D =V _{DRM} , T _j =125°C	5 500	uA	
V _{TM}	On-state voltage	I _T = 8A	1.5	V	
I _H	Holding current	I _{GT} = 0.5A, V _D = 12V	20	mA	

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