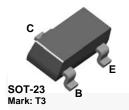


March 2014

# **BSS63 PNP General-Purpose Amplifier**

# **Description**

This device is designed for general-purpose amplifier and switch applications requiring high voltages. Sourced from process 74.



# **Ordering Information**

Part Number	Marking	Package	Packing Method
BSS63	Т3	SOT-23 3L	Tape and Reel

# Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-100	V
V <sub>CBO</sub>	Collector-Base Voltage	-110	V
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V
I <sub>C</sub>	Collector Current - Continuous	-200	mA
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature Range -55 to +150		°C

#### Notes

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

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## Thermal Characteristics(3)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation	350	mW
$P_{D}$	Derate Above T <sub>A</sub> = 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

#### Note:

3. Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -100 \mu\text{A},  I_B = 0$	-100		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -10  \mu A, I_E = 0$	-110		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -1.0 \mu\text{A},  I_C = 0$	-6.0		V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -90 \text{ V}, I_{E} = 0$		-100	nA
		$V_{CB} = -90 \text{ V}, I_E = 0,$ $T_A = 150^{\circ}\text{C}$		-50	μА
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = -6.0 \text{ V}, I_{C} = 0$		-200	nA
h <sub>FE</sub>	DC Current Gain	$I_C = -10 \text{ mA}, V_{CE} = -1.0 \text{ V}$	30		
		$I_C = -25 \text{ mA}, V_{CE} = -1.0 \text{ V}$	30		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -25 \text{ mA}, I_B = -2.5 \text{ mA}$		-0.25	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_C = -25 \text{ mA}, I_B = -2.5 \text{ mA}$		-0.9	V
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_C = 25 \text{ mA}, V_{CE} = -5.0 \text{ V},$ f = 35 MHz	50		MHz

# **Typical Performance Characteristics**

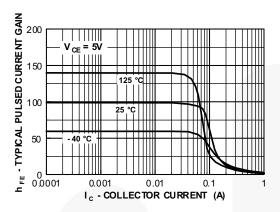


Figure 1. Typical Pulsed Current Gain vs. Collector Current

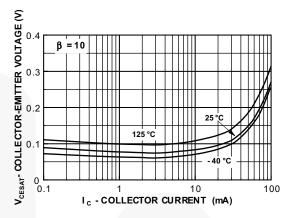


Figure 2. Collector-Emitter Saturation Voltage vs. Collector Current

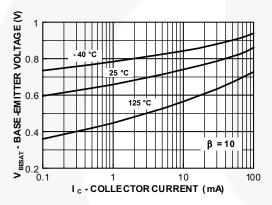


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

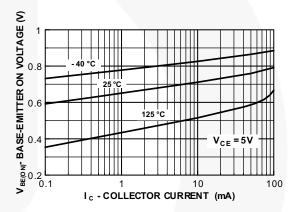


Figure 4. Base-Emitter On Voltage vs. Collector Current

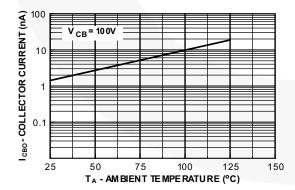


Figure 5. Collector Cut-Off Current vs.
Ambient Temperature

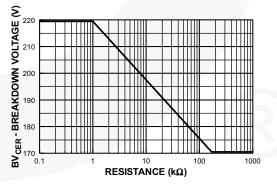


Figure 6. Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

# **Typical Performance Characteristics** (Continued)

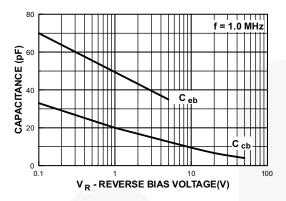


Figure 7. Input and Output Capacitance vs. Reverse Voltage

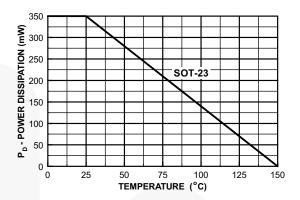


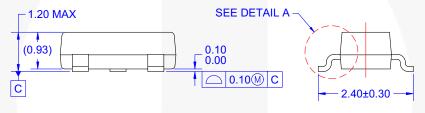
Figure 8. Power Dissipation vs. Ambient Temperature

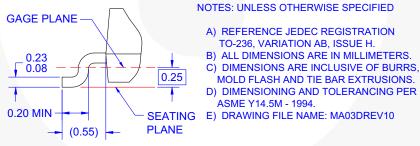
# **Physical Dimensions**

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LAND PATTERN RECOMMENDATION

SOT-23





DETAIL A

Figure 9. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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