

SOT-23 Formed SMD Package

BF820 BF822

SILICON EPITAXIAL TRANSISTORS

N-P-N transistors



ABSOLUTE MAXIMUM RATINGS

			BF820		BF822
Collector-base voltage (open emitter)	V _{CB0}	max.	300		250 V
Collector-emitter voltage (open base)	V_{CE0}	max.	-		250 V
Collector-emitter voltage ($R_{BE} = 2,7 \text{ kW}$)	V_{CER}	max.	<u>300</u>		V
Collector current (peak value)	I _{CM}	max.		100	mA
Total power dissipation up to $T_{amb} = 25 \ ^{\circ}C$	P _{tot}	max.		250	mW
Junction temperature	Tj	max.		150	° C
D.C. current gain					
$I_C = 25 \text{ mA}; V_{CE} = 20 \text{ V}$	h _{FE}	>		50	
Feedback capacitance at $f = 1$ MHz					
$I_{C} = 0; V_{CE} = 30 V$	C_{re}	<		1,6	pF
Transition frequency at $f = 35$ MHz					
$I_C = 10mA; V_{CE} = 10 V$	f_T	>		60	MHz
$I_C = 25 \text{ mA}; V_{CE} = 20 \text{ V}$ Feedback capacitance at $f = 1 \text{ MHz}$ $I_C = 0; V_{CE} = 30 \text{ V}$ Transition frequency at $f = 35 \text{ MHz}$	C _{re}	<		1,6	1

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BF820
BF822
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RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified) Limiting values

			BF820	BF8	822
Collector-base voltage (open emitter)	V _{CB0}	max.	300	25	$\overline{\theta} V$
Collector-emitter voltage (open base)	V_{CE0}	max.	-	25	0 V
Collector-emitter voltage ($R_{BE} = 2.7 \text{ kW}$)	V_{CER}	max.	300		-V
Emitter-base voltage (open collector)	V_{EB0}	max.		5	\overline{V}
Collector current (d.c.)	I_C	max.		50	mA
Collector current (peak value)	I _{CM}	max.		100	mA
Total power dissipation					
up to $T_{amb} = 25 \ ^{\circ}C$	P _{tot}	max.		250	mW
Storage temperature	T _{stg}		-55	to +150	°C
Junction temperature	T_j	max.		150	° C

R_{th j-a}

THERMAL RESISTANCE

From junction to ambient

CHARACTERISTICS

$T_i = 25$ °C unless otherwise specified
Collector cut-off current
$I_E = 0; V_{CB} = 200 V$
Collector-emitter voltage
R_{BE} = 2,7 kW; V_{CE} = 250 V
$R_{BE} = 2.7 k_W$; $V_{CE} = 200V$; $T_j = 150^{\circ}C$
Saturation voltage
$I_C = 30 mA; l_B = 5 mA$
D.C. current gain
$I_C = 25 mA; V_{CE} = 20 V$
Transition frequency at f = 35 MHz
$l_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}$
Feedback capacitance at $f = 1 MHz$
$I_C = 0; V_{CE} = 30 V$

U				
		BF820	BF8	22
I _{CB0}	<	10	10) nA
	< <	50 10		<i>D nA</i> <u>D</u> mA
V _{CEsat}	<		0,6	V
h _{FE}	>		50	
f_T	>		60	MHz

C_{re} < *1,6 pF*

500

K/W

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Data Sheet