

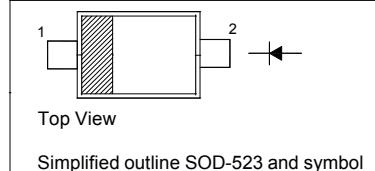
**GENERAL PURPOSE PIN DIODES**
**:95H F9**

- Low diode capacitance
- Low diode forward resistance
- Marking: O

**A U ]a i a 'FU]b[ g'UbX'9`YWF]WU'7\ UFUWF]gH]WgZG]b[ `Y8]cXY'**  
**4 HU1 & ) °C**

**PINNING**

PIN	DESCRIPTION
1	Cathode
2	Anode



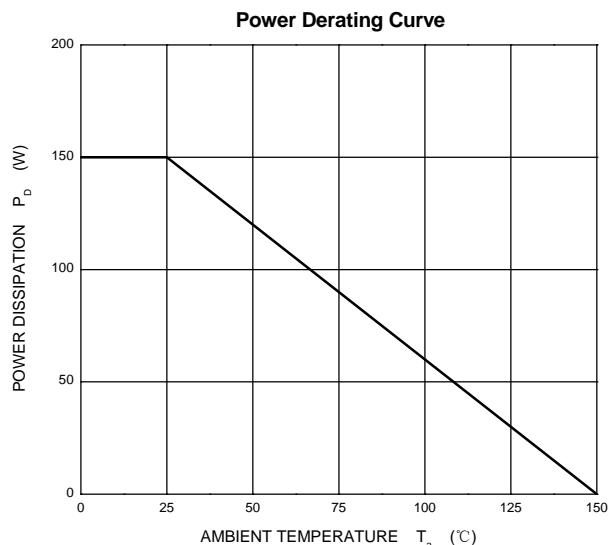
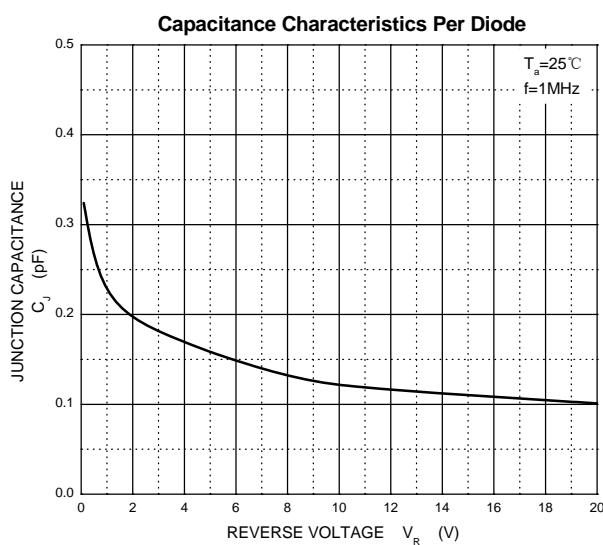
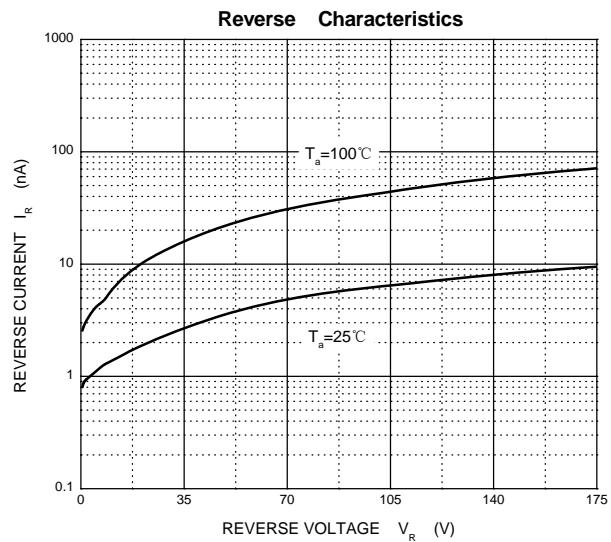
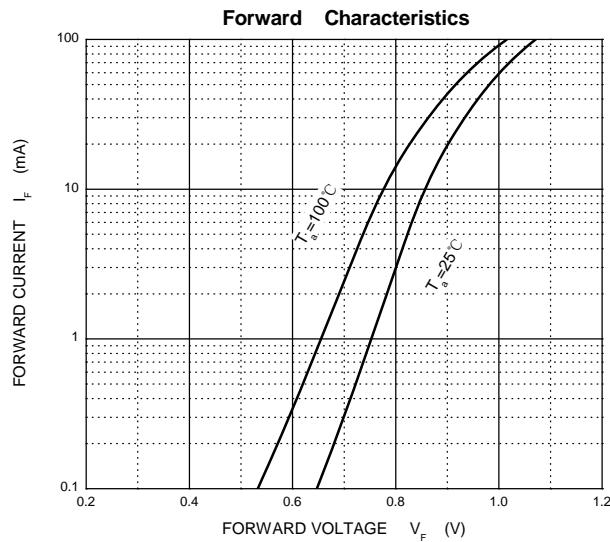
DUFUa YhYf	Gra Vc	@a Jh	I bjh
7cbHbi ci g'FYj YfgYJc`H[ Y	V <sub>R</sub>	150	V
7cbHbi ci g': cfk UFX'7 i ffYbh	I <sub>F</sub>	100	mA
Dck Yf'8]gg]dU]cb	P <sub>D</sub>	150	mW
H Yfa U'F Yg]gHbWZca 'i bW]cb'h	R <sub>θJS</sub>	85	°C/W
>i bW]cb'Hya dYfUh fY	T <sub>j</sub>	-55~+150	°C
GcfU YHYa dYfUh fY	T <sub>STG</sub>	-55~+150	°C

**9`YWF]WU'FU]b[ g'4 HU1 & ) °C**

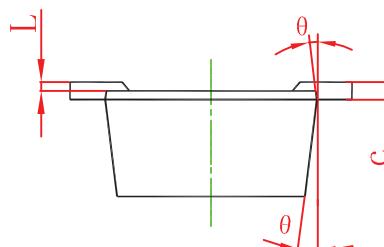
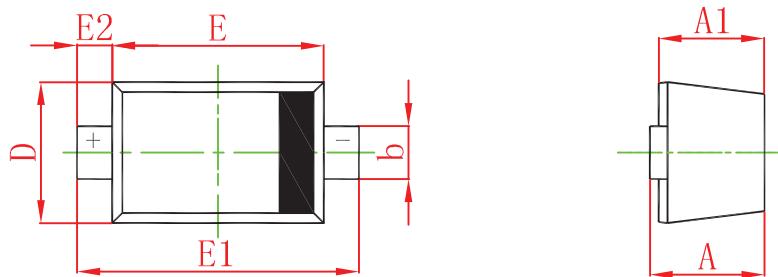
DUFUa YhYf	Gra Vc	A]b	Hrd	A U	I bjh	7cbX]Hcb
7cbHbi ci g'FYj YfgYJc`H[ Y	V <sub>R</sub>	150			V	I <sub>R</sub> =10μA
: cfk UFX'j c`H[ Y	V <sub>F</sub>			1.1	V	I <sub>F</sub> =50mA
FYj YfgYWffYbh	I <sub>R</sub>			10	uA	V <sub>R</sub> =150V
8]cXYWldUMHbW	C <sub>d1</sub>		0.4*		pF	V <sub>R</sub> =0V,f=1MHz
	C <sub>d2</sub>			0.55	pF	V <sub>R</sub> =1V,f=1MHz
	C <sub>d3</sub>			0.35	pF	V <sub>R</sub> =5V,f=1MHz
8]cXYZcfk UFX'fYg]gHbW	r <sub>D</sub>			9	Ω	I <sub>F</sub> =0.5mA , f=100MHz;note1
	r <sub>D</sub>			6.5	Ω	I <sub>F</sub> =1mA , f=100MHz;note1
	r <sub>D</sub>			2.5	Ω	I <sub>F</sub> =10mA , f=100MHz;note1

Note 1. Guaranteed on AQL basis: inspection level S4,AQL 1.0.

## Typical Characteristics

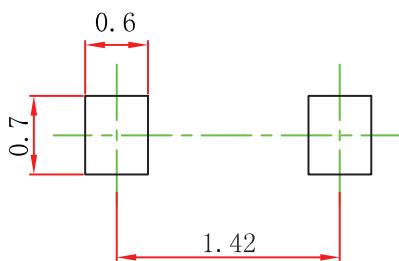


### SOD-523 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.510	0.770	0.020	0.031
A1	0.500	0.700	0.020	0.028
b	0.250	0.350	0.010	0.014
c	0.080	0.150	0.003	0.006
D	0.750	0.850	0.030	0.033
E	1.100	1.300	0.043	0.051
E1	1.500	1.700	0.059	0.067
E2	0.200 REF		0.008 REF	
L	0.010	0.070	0.001	0.003
θ	7° REF		7° REF	

### SOD-523 Suggested Pad Layout



#### Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.