0.075Ω

17A^⑤

International

Features

- Advanced Planar Technology
- Low On-Resistance
- Dynamic dV/dT Rating
- 175°C Operating Temperature
- Fast Switching

Description

applications.

- Fully Avalanche Rated
- Repetitive Avalanche Allowed up to Tjmax

Specifically designed for Automotive applications, this Cellular design of HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-

resistance per silicon area. This benefit combined with the

fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and a wide variety of other

- Lead-Free, RoHS Compliant
- Automotive Qualified *

HEXFET

UIRFU024N					
HEXFET [®] Power	r MOSFET	Г			
V _{(BR)DSS}	55V				

max.

AUIRFR024N



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	17	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	12	А
I _{DM}	Pulsed Drain Current ^① ^⑥	68	
P _D @T _C = 25°C	Power Dissipation	45	W
	Linear Derating Factor	0.3	W/°C
V _{GS} Gate-to-Source Voltage		± 20	V
E _{AS}	Single Pulse Avalanche Energy 26	71	mJ
I _{AR}	Avalanche Current ①	10	А
E _{AR} Repetitive Avalanche Energy ①		4.5	mJ
dv/dt	Peak Diode Recovery dv/dt 36	5.0	V/ns
TJ	Operating Junction and	-55 to + 175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{0JC}	Junction-to-Case	_	3.3	
$R_{ ext{ heta}JA}$	Junction-to-Ambient (PCB mount) **		50	°C/W
R _{0JA}	Junction-to-Ambient		110	

HEXFET[®] is a registered trademark of International Rectifier. *Qualification standards can be found at http://www.irf.com/ www.irf.com

Static Electrical Characteristics @ $T_J = 25^{\circ}C$ (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	55			V	$V_{GS} = 0V, I_D = 250 \mu A$
$\Delta V_{(BR)DSS} / \Delta T_J$	Breakdown Voltage Temp. Coefficient		0.052		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.075	Ω	V _{GS} = 10V, I _D = 10A ④
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
gfs	Forward Transconductance	4.5			S	V _{DS} = 25V, I _D = 10A ⑥
I _{DSS}	Drain-to-Source Leakage Current			25	μA	$V_{DS} = 55V, V_{GS} = 0V$
				250		$V_{DS} = 44V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage			-100		$V_{GS} = -20V$

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

Total Gate Charge			20		I _D = 10A
Gate-to-Source Charge			5.3	nC	$V_{DS} = 44V$
Gate-to-Drain ("Miller") Charge			7.6		V _{GS} = 10V,See Fig 6 and 13 ⊕®
Turn-On Delay Time		4.9			$V_{DD} = 28V$
Rise Time		34			I _D = 10A
Turn-Off Delay Time		19		ns	$R_{G} = 24 \Omega$,
Fall Time		27			$R_D = 2.6\Omega$, See Fig.10 \textcircled{G}
Internal Drain Inductance		4.5		nH	Between lead, 6mm (0.25in.)
Internal Source Inductance		7.5			from package
Input Capacitance		370			V _{GS} = 0V
Output Capacitance		140		рF	$V_{DS} = 25V$
Reverse Transfer Capacitance		65			f = 1.0MHz, See Fig.5
	Gate-to-Source Charge Gate-to-Drain ("Miller") Charge Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Internal Drain Inductance Internal Source Inductance Input Capacitance Output Capacitance	Gate-to-Source Charge Gate-to-Drain ("Miller") Charge Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Internal Drain Inductance Internal Source Inductance Input Capacitance Output Capacitance	Gate-to-Source Charge — — Gate-to-Drain ("Miller") Charge — — Turn-On Delay Time — 4.9 Rise Time — 34 Turn-Off Delay Time — 19 Fall Time — 27 Internal Drain Inductance — 4.5 Internal Source Inductance — 370 Output Capacitance — 140	Gate-to-Source Charge5.3Gate-to-Drain ("Miller") Charge7.6Turn-On Delay Time4.9Rise Time34Turn-Off Delay Time19Fall Time27Internal Drain Inductance4.5Internal Source Inductance7.5Input Capacitance370Output Capacitance140	Gate-to-Source Charge 5.3 nC Gate-to-Drain ("Miller") Charge 7.6 Turn-On Delay Time 4.9 Rise Time 34 Turn-Off Delay Time 19 Fall Time 27 Internal Drain Inductance 4.5 Internal Source Inductance 370 Input Capacitance 370 Output Capacitance 140

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current			17 ⑤		MOSFET symbol
	(Body Diode)		170		А	showing the
I _{SM}	Pulsed Source Current			68		integral reverse
	(Body Diode) ①			00		p-n junction diode.
V _{SD}	Diode Forward Voltage			1.3	V	$T_{J} = 25^{\circ}C, I_{S} = 10A, V_{GS} = 0V$ (4)
t _{rr}	Reverse Recovery Time		56	83	ns	T _J = 25°C, I _F = 10A
Q _{rr}	Reverse Recovery Charge		120	180	nC	di/dt = 100A/µs ④⑥
t _{on}	Forward Turn-On Time	Intrinsic	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- O V_{DD} = 25V, starting T_J = 25°C, L = 1mH, R_G = 25\Omega, I_{AS} = 10A. (See Figure 12)
- 3 I_{SD} \leq 10A, di/dt \leq 280A/µs, V_{DD} \leq V_{(BR)DSS}, T_J \leq 175°C
- ④ Pulse width \leq 300µs; duty cycle \leq 2%
- $\ensuremath{\textcircled{S}}$ This is applied for I-PAK, L_S of D-PAK is measured between lead and center of die contact .
- © Uses IRFZ24N data and test conditions.

** When mounted on 1" square PCB (FR-4 or G-10 Material) . For recommended footprint and soldering techniques refer to application note #AN-994

Qualification Information[†]

		Automotive				
		(per AEC-Q101) ^{††}				
Qualification Leve	el	qualification.	This part number(s) passed Automotive IR's Industrial and Consumer qualification of by extension of the higher Automotive level.			
Moisture Sensitiv	Moisture Sensitivity Level		MSL1			
			N/A			
	Machine Model	Class M2(+/-150V) ^{†††}				
		AEC-Q101-002				
	Human Body Model	Class H1A(+/-500V) ^{†††}				
ESD	ESD Charged Device		AEC-Q101-001			
			Class C5(+/-2000V) ^{†††}			
	Model	AEC-Q101-005				
RoHS Compliant		Yes				

† Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

† Exceptions to AEC-Q101 requirements are noted in the qualification report.

††† Highest passing voltage



Fig 1. Typical Output Characteristics

Fig 3. Typical Transfer Characteristics



Fig 2. Typical Output Characteristics



Fig 4. Normalized On-Resistance Vs. Temperature



f = 1MHz

International

TOR Rectifier

700



Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage



Fig 7. Typical Source-Drain Diode Forward Voltage



Fig 8. Maximum Safe Operating Area







Fig 10a. Switching Time Test Circuit



Fig 10b. Switching Time Waveforms



Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Т_D 4.2А

7.2A

10A

175





-

Fig 13a. Basic Gate Charge Waveform

Charge

10 V



Fig 13b. Gate Charge Test Circuit



Peak Diode Recovery dv/dt Test Circuit

* V_{GS} = 5V for Logic Level Devices

Fig 14. For N-Channel HEXFETS

D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)









NOTES;

- 1.- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2.- DIMENSION ARE SHOWN IN INCHES [MILLIMETERS].
- A- LEAD DIMENSION UNCONTROLLED IN L5.
- A- DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
- 5.- SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.
- A DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTWOST EXTREMES OF THE PLASTIC BODY. A- DIMENSION 61 & c1 APPLIED TO BASE METAL ONLY.
- A- DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 9.- OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA

S Y M	DIMENSIC		DIMENSIONS					
B 0	MILLIM	ETERS	INC	O T E S				
Ľ	MIN.	MAX,	MIN.	MAX.	S			
A	2.18	2.39	.086	.094				
A1	-	0,13	-	.005				
b	0.64	0.89	.025	.035				
ь1	0.65	0.79	.025	.031	7			
b2	0.76	1.14	.030	.045				
b3	4.95	5,46	.195	.215	4			
с	0,46	0.61	.018	.024				
c1	0.41	0,56	.016	.022	7			
c2	0,46	0.89	.018	.035				
D	5,97	6.22	.235	.245	6			
D1	5.21	-	.205	-	4			
E	6,35	6.73	.250	.265	6			
E1	4.32	-	.170	-	4			
e	2.29	BSC	.090	BSC				
н	9.40	10.41	.370	.410				
L	1.40	1,78	.055	.070				
L1	2,74	BSC	.108	REF.				
L2	0.51	BSC	.020 BSC					
L3	0.89	1.27	.035	.050	4			
L4	-	1.02	-	.040				
L5	1.14	1.52	.045	.060	3			
ø	0*	10*	0*	10*				
ø1	0'	15*	0'	15*				
ø2	25'	35*	25'	35*				

LEAD ASSIGNMENTS

<u>HEXFET</u>

1.- GATE 2.- DRAIN 3 - SOURCE 4.- DRAIN

IGBT & CoPAK

1.- GATE 2.- COLLECTOR

3.- EMITTER 4.- COLLECTOR

D-Pak (TO-252AA) Part Marking Information



1/A

(ь)

I-Pak (TO-251AA) Package Outline (Dimensions are shown in millimeters (inches)

A c2 ⊕0.010 (0.025)@CAB b4 в D С SEATING PLANE L1 L3 çĮ Ę Ь2 C Ш A1 3× b ⊕ 0.010 (0.25)@CAB e 2×



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERWOST EXTREMES OF THE PLASTIC BODY.
- 4 THERMAL PAD CONTOUR OPTION WITHIN DIMENSION 64, L2, E1 & D1.
- 5 LEAD DIMENSION UNCONTROLLED IN L3.
- 6 DIMENSION 61, 63 APPLY TO BASE METAL ONLY. 7 OUTLINE CONFORMS TO JEDEC OUTLINE TO-251AA.
- 7 OUTLINE CONFORMS TO JEDEC OUTLINE TO-251A 8 CONTROLLING DIMENSION : INCHES.

LEAD ASSIGNMENTS

1.- GATE

2.- DRAIN 3.- SOURCE

4.- DRAIN

		DIMEN	SIONS		
SYMBOL	MILLIN	ETERS	INC	HES	
	Min.	MAX,	MIN.	MAX,	NOTES
A	2.18	2.39	0.086	.094	
A1	0.89	1,14	0.035	0.045	
b	0.64	0.89	0.025	0.035	
ь1	0.64	0,79	0.025	0.031	4
b2	0,76	1,14	0.030	0.045	
b3	0,76	1,04	0.030	0.041	
b4	5.00	5,46	0,195	0,215	4
c	0,46	0.61	0.018	0.024	
c1	0.41	0,56	0.016	0.022	
c2	.046	0.86	0.018	0.035	
D	5.97	6.22	0.235	0.245	3, 4
D1	5.21	-	0.205	-	4
E	6.35	6.73	0.250	0.265	3, 4
E1	4,32	-	0,170	-	4
е	2.	29	0.090	BSC	
L	8.89	9.60	0.350	0.380	
L1	1,91	2.29	0,075	0,090	
L2	0.89	1,27	0.035	0,050	4
L3	1,14	1.52	0.045	0.060	5
ø1	ď	15'	ď	15*	

I-Pak (TO-251AA) Part Marking Information



D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES :

- 1. CONTROLLING DIMENSION : MILLIMETER.
- 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. OUTLINE CONFORMS TO EIA-481.

Ordering Information

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRFR024N	DPak	Tube	75	AUIRFR024N
		Tape and Reel	2000	AUIRFR024NTR
		Tape and Reel Left	3000	AUIRFR024NTRL
		Tape and Reel Right	3000	AUIRFR024NTRR
AUIRFU024N	IPak	Tube	75	AUIRFU024N

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