

ACST8

Overvoltage protected AC switch

Features

- Triac with overvoltage protection
- High noise immunity: static dV/dt > 2000 V/µs
- TO-220FPAB insulated package: 1500 V rms

Benefits

- Enables equipment to meet IEC 61000-4-5
- High off-state reliability with planar technology
- Needs no external overvoltage protection
- Reduces the power passive component count
- High immunity against fast transients described in IEC 61000-4-4 standards

Applications

- AC mains static switching in appliance and industrial control systems
- Drive of medium power AC loads such as:
 - Universal motor of washing machine drum
 - Compressor for fridge or air conditioner

Description

The ACST8 series belongs to the ACS[™]/ ACST power switch family built around A.S.D.[®] (application specific discrete) technology. This high performance device is suited to home appliances or industrial systems and drives an induction motor up to 8 A.

This ACST8 device embeds a Triac structure with a high voltage clamping device to absorb the inductive turn off energy and withstand line transients such as those described in the IEC 61000-4-5 standards.

ACST8 shows a high noise immunity complying with IEC standards such as IEC 61000-4-4 (fast transient burst test).







Table 1. Device summary

Symbol	Value	Unit
I _{T(RMS)}	8	А
V _{DRM} /V _{RRM}	800	V
I _{GT}	30	mA

TM: ACS is a trademark of STMicroelectronics.

®: A.S.D. is a registered trademark of STMicroelectronics

1 Characteristics

Symbol	Parame	Value	Unit		
		TO-220FPAB	T _{case} = 91 °C		
I _{T(RMS)}		TO-220AB / D ² PAK	T _{case} = 105 °C	8	A
		D ² PAK with 1 cm ² Cu	T _{amb} = 43 °C	2	A
	Non repetitive surge peak on-state	F = 50 Hz	t _p = 20 ms	80	А
I _{TSM}	current T_j initial = 25 °C, full cycle sine wave	F = 60 Hz	t _p = 16.7 ms	84	А
l ² t	Thermal constraint for fuse selection	t _p = 10 ms	42	A ² s	
dl/dt	Non repetitive on-state current critical rate of rise $I_G = 10 \text{ mA} (t_r < 100 \text{ ns})$		Rate period > 1 mn	100	A/µs
$V_{PP}^{(1)}$	Non repetitive line peak pulse voltage		T _j = 25 °C	2	kV
P _{G(AV)}	Average gate power dissipation		T _j = 125 °C	0.1	W
P _{GM}	Peak gate power dissipation ($t_p = 20 \text{ ms}$)		T _j = 125 °C	10	W
I _{GM}	Peak gate current ($t_p = 20 \text{ ms}$) $T_j = 125 \text{ °C}$			1.6	А
T _{stg}	Storage temperature range	- 40 to + 150	°C		
Tj	Operating junction temperature range	- 40 to + 125	°C		
Τ _Ι	Maximum lead soldering temperature during 10 s			260	°C
V _{INS(RMS)}	Insulation rms voltage TO-220FPAB			1500	V

1. According to test described in IEC 61000-4-5 standard and *Figure 18.*

Table 3. Electrical characteristics per switch

Symbol	Test conditions	Quadrant	Тј		Value	Unit
I _{GT} ⁽¹⁾	V_{OUT} = 12 V, R _L = 33 Ω	- -	25 °C	Max	30	mA
V _{GT}	V_{OUT} = 12V, R_L = 33 Ω	- -	25 °C	Max	1.0	V
V _{GD}	$V_{OUT} = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	- -	125 °C	Min	0.2	V
I _H ⁽²⁾	I _{OUT} = 500 mA		25 °C	Max	30	mA
I _L	$I_{G} = 1.2 \text{ x } I_{GT}$	1 - 11 - 111	25 °C	Max	50	mA
dV/dt ⁽²⁾	$V_{OUT} = 67\% V_{DRM}$, gate open		125 °C	Min	2000	V/µs
(dl/dt)c ⁽²⁾	Without snubber		125 °C	Min	8	A/ms
V _{CL}	$I_{CL} = 0.1 \text{ mA}, t_p = 1 \text{ ms}$		25 °C	Min	850	V

1. Minimum I_{GT} is guaranteed at 5% of $I_{GT(Max)}$

2. For either positive or negative polarity of OUT pin with reference to COM pin



Symbol	Test conditions		Value	Unit	
V_{TM}	$I_{TM} = 11.3 \text{ A t}_{p} = 500 \ \mu \text{s}$	Tj = 25 °C	Max	1.5	V
V _{TO}	Threshold voltage	Tj = 125 °C	Max	0.9	V
R _D	Dynamic resistance	Tj = 125 °C	Max	50	mΩ
I _{DRM}		Tj = 25 °C	Мах	20	μA
$V_{OUT} = V_{DRM} / V_{RRM}$	VOUT = VDRM / VRRM	Tj = 125 °C	IVIAX	1	mA

Table 4.Static characteristics

Table 5.Thermal resistances

Symbol	Parameter		Value	Unit
Junction to ambient		TO-220FPAB TO-220AB	60	
	Junction to ambient (soldered on 1 cm ² copper pad)	D ² PAK	45	°C/W
В	lunction to cope (AC)	TO-220FPAB	3.6	
R _{th(j-c)}	Junction to case (AC)	TO-220AB, D ² PAK	2	

Figure 2. Maximum power dissipation versus Figure 3. on-state rms current

On-state rms current versus case temperature (full cycle)











Figure 6. Relative variation of gate trigger current (I_{GT}) and voltage (V_{GT}) versus junction temperature



Figure 8. Surge peak on-state current versus Figure 9. number of cycles









Doc ID 7463 Rev 7

ACST8

Figure 10. On-state characteristics (maximum Figure 11. values)





Figure 12. Relative variation of static dV/dt immunity versus junction temperature (gate open)



Figure 14. Relative variation of clamping voltage (V_{CL}) versus junction temperature (minimum values)











2 Application information

2.1 Typical application description

The ACST8 device has been designed to control medium power load, such as AC motors in home appliances. Thanks to its thermal and turn off commutation performances, the ACST8 switch is able to drive an inductive load up to 8 A with no turn off additional snubber. It also provides high thermal performances in static and transient modes such as high torque operating conditions or inrush current of an AC motor.



Figure 16. AC induction motor control – typical diagram



2.2 AC line transient voltage ruggedness

In comparison with standard Triacs, which are not robust against surge voltage, the ACST8 is self-protected against over-voltage, specified by the new parameter V_{CL} . The ACST8 switch can safely withstand AC line transient voltages either by clamping the low energy spikes, such as inductive spikes at switch off, or by switching to the on state (for less than 10 ms) to dissipate higher energy shocks through the load. This safety feature works even with high turn-on current ramp up.

The test circuit of *Figure 17* represents the ACST8 application, and is used to stress the ACST switch according to the IEC 61000-4-5 standard conditions. With the additional effect of the load which is limiting the current, the ACST switch withstands the voltage spikes up to 2 kV on top of the peak line voltage. The protection is based on an overvoltage crowbar technology. The ACST8 folds back safely to the on state as shown in *Figure 18*. The ACST8 recovers its blocking voltage capability after the surge and the next zero current crossing. Such a non repetitive test can be done at least 10 times on each AC line voltage polarity.

Figure 17. Overvoltage ruggedness test circuit for resistive and inductive loads for IEC 61000-4-5 standards







Figure 18. Typical current and voltage waveforms across the ACST8 during IEC 61000-4-5 standard test

3 Ordering information scheme

Figure 19. Ordering information scheme





57

4 Package information

- Epoxy meets UL94, V0
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

Table 6. TO-220AB dimensions



		Dimensions			
	Ref.	Millin	neters	Inches	
		Min.	Max.	Min.	Max.
	А	4.4	4.6	0.173	0.181
	В	2.5	2.7	0.098	0.106
	D	2.5	2.75	0.098	0.108
	E	0.45	0.70	0.018	0.027
Dia	F	0.75	1	0.030	0.039
	F1	1.15	1.70	0.045	0.067
L2 L7	F2	1.15	1.70	0.045	0.067
	G	4.95	5.20	0.195	0.205
	G1	2.4	2.7	0.094	0.106
$ \begin{array}{c c} & & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & & & & \\ & & & \\ & & & & & \\ & & & & \\ & & & & & \\ & & & & & \\ & & & & \\ & & & & & \\ & & & & & \\ & & & $	Н	10	10.4	0.393	0.409
L4 + F2	L2	16	Тур.	0.63	Тур.
	L3	28.6	30.6	1.126	1.205
G1 ↔	L4	9.8	10.6	0.386	0.417
' <mark>← G</mark> [→]	L5	2.9	3.6	0.114	0.142
	L6	15.9	16.4	0.626	0.646
	L7	9.00	9.30	0.354	0.366
	Diam.	3.00	3.20	0.118	0.126

Table 7. TO-220FPAB dimensions



		Dimensions			
	Ref.	Millin	neters	Inches	
		Min.	Max.	Min.	Max.
	А	4.40	4.60	0.173	0.181
	A1	2.49	2.69	0.098	0.106
$\begin{array}{c} L_2 \\ \hline \\ $	A2	0.03	0.23	0.001	0.009
	В	0.70	0.93	0.027	0.037
	B2	1.14	1.70	0.045	0.067
	С	0.45	0.60	0.017	0.024
	C2	1.23	1.36	0.048	0.054
	D	8.95	9.35	0.352	0.368
G	E	10.00	10.40	0.393	0.409
A2	G	4.88	5.28	0.192	0.208
	L	15.00	15.85	0.590	0.624
M ×	L2	1.27	1.40	0.050	0.055
+ FLAT ZONE NO LESS THAN 2mm	L3	1.40	1.75	0.055	0.069
FERI ZONE NO LESS THAN ZIIIII	М	2.40	3.20	0.094	0.126
	R	0.40	typ.	0.01	6 typ.
	V2	0°	8°	0°	8°

Table 8.D²PAK dimensions





5 Ordering information

Table 9. Ordering information

Order code	Marking	Package	Weight	Base qty	Packing mode
ACST830-8FP		TO-220FPAB	2.4 g	50	Tube
ACST830-8T	ACST8308	TO-220AB	2.3 g	50	Tube
ACST830-8GTR		D ² PAK	1.5 g	500	Tape and reel

6 Revision history

Table 10. Document revision history

Date	Revision	Changes
Jan-2002	4B	Last update.
08-Nov-2004	5	TO-220AB and D ² PAK packages added.
24-Nov-2004	6	Table 6 page 3: I _{GT} parameter added
18-Dec-2009	7	Added ECOPACK statement. Reformatted for consistency with other datasheets in this product class. Order codes updated.



Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2009 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan -Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com



Doc ID 7463 Rev 7