# 74LVC1G11

## Single 3-input AND gate

Rev. 13 — 9 May 2023

**Product data sheet** 

### 1. General description

The 74LVC1G11 is a single 3-input AND gate. Inputs can be driven from either 3.3~V or 5~V devices. This feature allows the use of these devices as translators in mixed 3.3~V and 5~V environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times.

This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

#### 2. Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- ±24 mA output drive (V<sub>CC</sub> = 3.0 V)
- CMOS low power dissipation
- · Direct interface with TTL levels
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- · Latch-up performance exceeds 250 mA
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8C (2.7 V to 3.6 V)
  - JESD36 (4.5 V to 5.5 V)
- ESD protection:
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



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## 3. Ordering information

**Table 1. Ordering information** 

Type number	Package			
	Temperature range	Name	Description	Version
74LVC1G11GW	-40 °C to +125 °C	TSSOP6	plastic thin shrink small outline package; 6 leads; body width 1.25 mm	SOT363-2
74LVC1G11GV	-40 °C to +125 °C	SC-74; TSOP6	plastic surface-mounted package; 6 leads	SOT457
74LVC1G11GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74LVC1G11GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115
74LVC1G11GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202
74LVC1G11GX	-40 °C to +125 °C	X2SON6	plastic thermal enhanced extremely thin small outline package; no leads; 6 terminals; body 1.0 × 0.8 × 0.32 mm	SOT1255-2

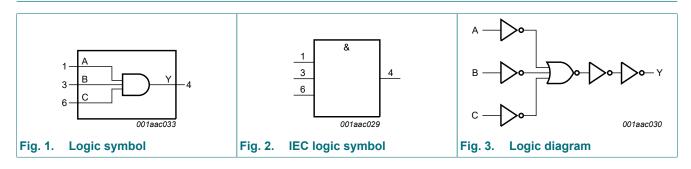
## 4. Marking

Table 2. Marking

Type number	Marking code[1]
74LVC1G11GW	VU
74LVC1G11GV	V11
74LVC1G11GM	VU
74LVC1G11GN	VU
74LVC1G11GS	VU
74LVC1G11GX	VU

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

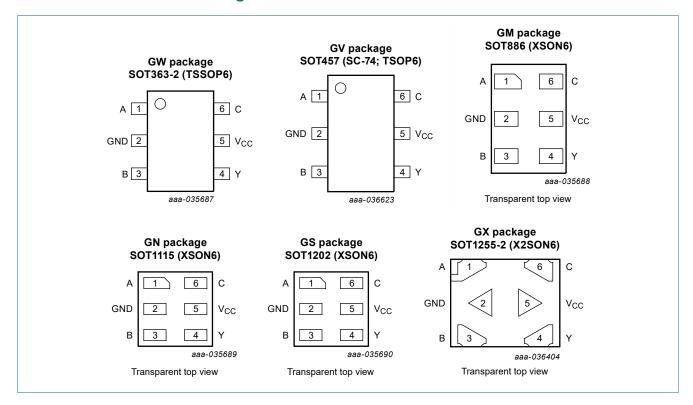
## 5. Functional diagram



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## 6. Pinning information

#### 6.1. Pinning



#### 6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
A	1	data input
GND	2	ground (0 V)
В	3	data input
Υ	4	data output
V <sub>CC</sub>	5	supply voltage
С	6	data input

## 7. Functional description

#### **Table 4. Function table**

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$ 

Input	Output		
Α	В	С	Υ
Н	Н	Н	Н
L	X	X	L
X	L	Х	L
Х	X	L	L

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+6.5	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mΑ
VI	input voltage	[1]	-0.5	+6.5	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> > V <sub>CC</sub> or V <sub>O</sub> < 0 V	-	±50	mΑ
Vo	output voltage	Active mode [1]	-0.5	V <sub>CC</sub> + 0.5	V
		Power-down mode; V <sub>CC</sub> = 0 V [1]	-0.5	+6.5	V
I <sub>O</sub>	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>	-	±50	mΑ
I <sub>CC</sub>	supply current		-	100	mΑ
I <sub>GND</sub>	ground current		-100	-	mΑ
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C to } +125  ^{\circ}\text{C}$ [2]	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT457 (SC-74; TSOP6) package: Ptot derates linearly with 4.1 mW/K above 89 °C.

For SOT886 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package:  $P_{tot}$  derates linearly with 3.2 mW/K above 71  $^{\circ}\text{C}.$ 

For SOT1202 (XSON6) package:  $P_{tot}$  derates linearly with 3.3 mW/K above 74 °C.

For SOT1255-2 (X2SON6) package: Ptot derates linearly with 3.3 mW/K above 75 °C.

## 9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 1.65 V to 2.7 V	-	-	20	ns/V
		V <sub>CC</sub> = 2.7 V to 5.5 V	-	-	10	ns/V

<sup>[2]</sup> For SOT363-2 (TSSOP6) package: Ptot derates linearly with 3.7 mW/K above 83 °C.

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## 10. Static characteristics

**Table 7. Static characteristics** 

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	°C to +8	5 °C	-40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max	1
V <sub>IH</sub>	HIGH-level	V <sub>CC</sub> = 1.65 V to 1.95 V	0.65 × V <sub>CC</sub>	-	-	0.65 × V <sub>CC</sub>	-	V
	input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	-	-	1.7	-	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	2.0	-	-	2.0	-	V
		V <sub>CC</sub> = 4.5 V to 5.5 V	0.7 × V <sub>CC</sub>	-	-	0.7 × V <sub>CC</sub>	-	V
V <sub>IL</sub>	LOW-level	V <sub>CC</sub> = 1.65 V to 1.95 V	-	-	0.35 × V <sub>CC</sub>	-	0.35 × V <sub>CC</sub>	V
	input voltage	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	-	0.7	V
		V <sub>CC</sub> = 2.7 V to 3.6 V	-	-	0.8	-	0.8	V
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	0.3 × V <sub>CC</sub>	-	0.3 × V <sub>CC</sub>	V
V <sub>OH</sub>	HIGH-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
	output voltage	I <sub>O</sub> = -100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> - 0.1	-	V
		I <sub>O</sub> = -4 mA; V <sub>CC</sub> = 1.65 V	1.2	1.54	-	0.95	-	V
		I <sub>O</sub> = -8 mA; V <sub>CC</sub> = 2.3 V	1.9	2.15	-	1.7	-	V
		I <sub>O</sub> = -12 mA; V <sub>CC</sub> = 2.7 V	2.2	2.50	-	1.9	-	٧
		I <sub>O</sub> = -24 mA; V <sub>CC</sub> = 3.0 V	2.3	2.62	-	2.0	-	٧
		I <sub>O</sub> = -32 mA; V <sub>CC</sub> = 4.5 V	3.8	4.11	-	3.4	-	V
V <sub>OL</sub>	LOW-level	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>						
	output voltage	I <sub>O</sub> = 100 μA; V <sub>CC</sub> = 1.65 V to 5.5 V	-	-	0.10	-	0.10	V
		I <sub>O</sub> = 4 mA; V <sub>CC</sub> = 1.65 V	-	0.07	0.45	-	0.70	V
		I <sub>O</sub> = 8 mA; V <sub>CC</sub> = 2.3 V	-	0.12	0.30	-	0.45	V
		I <sub>O</sub> = 12 mA; V <sub>CC</sub> = 2.7 V	-	0.17	0.40	-	0.60	V
		I <sub>O</sub> = 24 mA; V <sub>CC</sub> = 3.0 V	-	0.33	0.55	-	0.80	V
		I <sub>O</sub> = 32 mA; V <sub>CC</sub> = 4.5 V	-	0.39	0.55	-	0.80	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	±0.1	±1	-	±1	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 5.5 \text{ V}$ ; $V_{CC} = 0 \text{ V}$	-	±0.1	±2	-	±2	μΑ
I <sub>CC</sub>	supply current	V <sub>I</sub> = 5.5 V or GND; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 1.65 V to 5.5 V	-	0.1	4	-	4	μA
ΔI <sub>CC</sub>	additional supply current	V <sub>I</sub> = V <sub>CC</sub> - 0.6 V; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 2.3 V to 5.5 V; per pin	-	5	500	-	500	μA
Cı	input capacitance	$V_{CC}$ = 3.3 V; $V_{I}$ = GND to $V_{CC}$	-	4	-	-	-	pF

<sup>[1]</sup> All typical values are measured at  $T_{amb}$  = 25 °C.

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## 11. Dynamic characteristics

#### **Table 8. Dynamic characteristics**

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
t <sub>pd</sub>	propagation delay	A, B and C to Y; see Fig. 4 [2]						
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.5	4.7	17.2	1.5	21.5	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.0	3.0	6.2	1.0	7.8	ns
		V <sub>CC</sub> = 2.7 V	1.0	3.0	6.0	1.0	7.5	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	2.6	4.9	1.0	6.2	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V	1.0	1.9	3.5	1.0	4.4	ns
C <sub>PD</sub>	power dissipation capacitance	$V_I = GND \text{ to } V_{CC}; V_{CC} = 3.3 \text{ V}$ [3]	-	13	-	-	-	pF

- [1] Typical values are measured at T<sub>amb</sub> = 25 °C and V<sub>CC</sub> = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.
- [2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu$ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

f<sub>i</sub> = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

#### 11.1. Waveform and test circuit

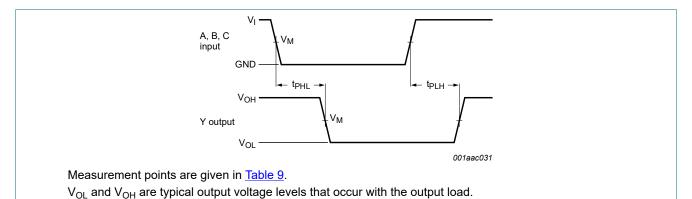
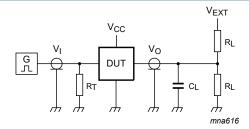


Fig. 4. The input (A, B, C) to output (Y) propagation delays

**Table 9. Measurement points** 

Supply voltage	Input	Output
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>
1.65 V to 1.95 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>
2.3 V to 2.7 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>

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Test data is given in Table 10.

Definitions for test circuit:

R<sub>L</sub> = Load resistance;

 $C_L$  = Load capacitance including jig and probe capacitance;

R<sub>T</sub> = Termination resistance should be equal to the output impedance Z<sub>o</sub> of the pulse generator;

 $V_{\text{EXT}}$  = External voltage for measuring switching times.

#### Fig. 5. Test circuit for measuring switching times

#### Table 10. Test data

Supply voltage	Input		Load		V <sub>EXT</sub>
V <sub>CC</sub>	VI	$t_r = t_f$	CL	R <sub>L</sub>	t <sub>PLH</sub> , t <sub>PHL</sub>
1.65 V to 1.95 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	1 kΩ	open
2.3 V to 2.7 V	V <sub>CC</sub>	≤ 2.0 ns	30 pF	500 Ω	open
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
4.5 V to 5.5 V	V <sub>CC</sub>	≤ 2.5 ns	50 pF	500 Ω	open

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## 12. Package outline

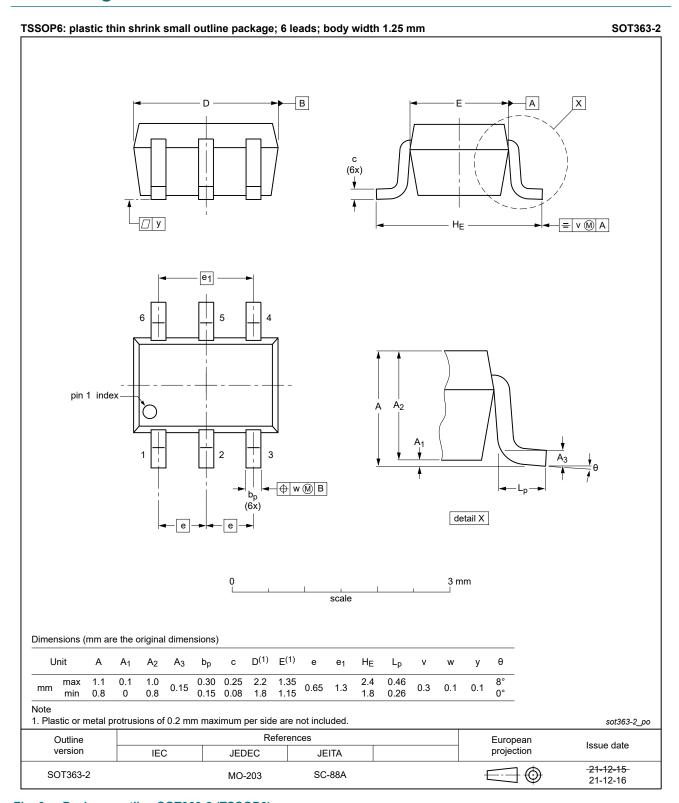


Fig. 6. Package outline SOT363-2 (TSSOP6)

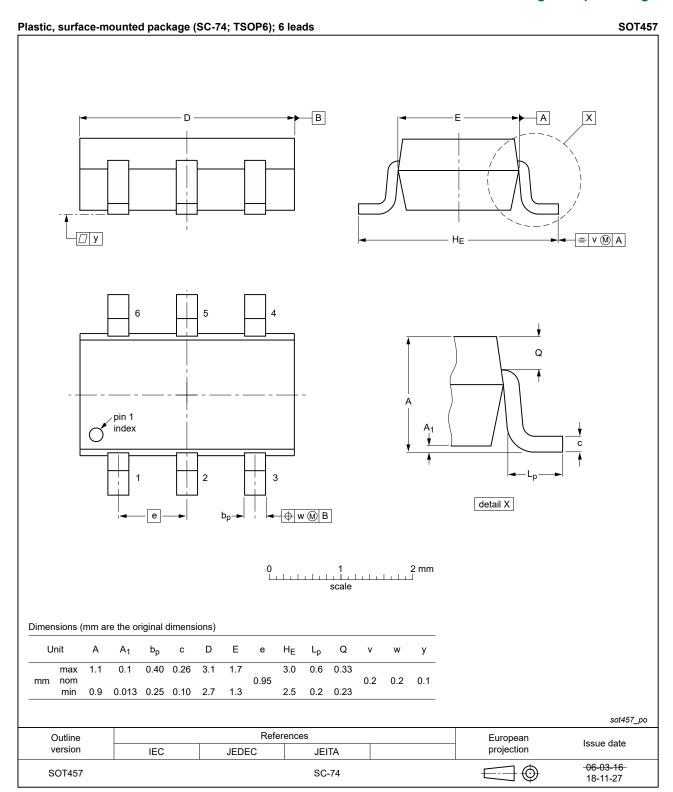


Fig. 7. Package outline SOT457 (SC-74; TSOP6)

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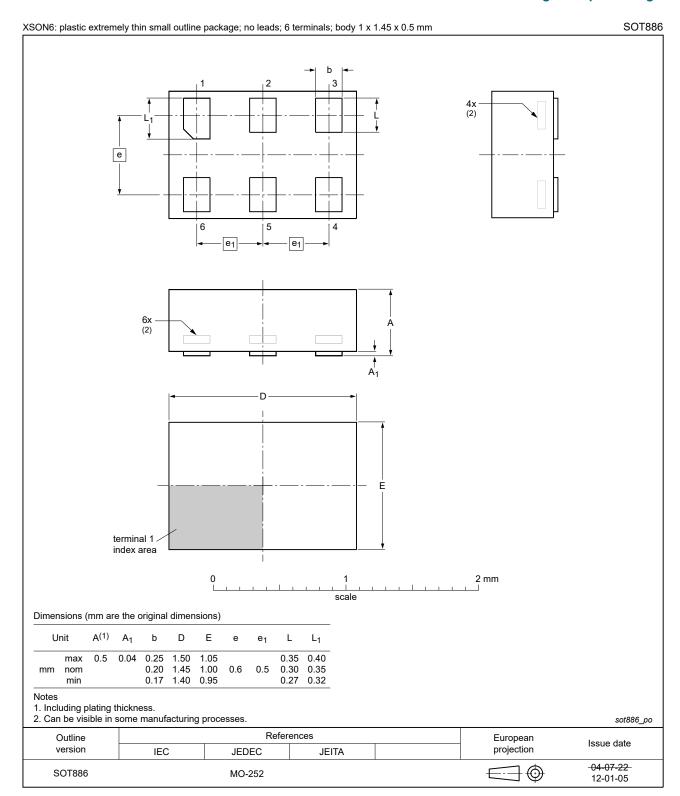


Fig. 8. Package outline SOT886 (XSON6)

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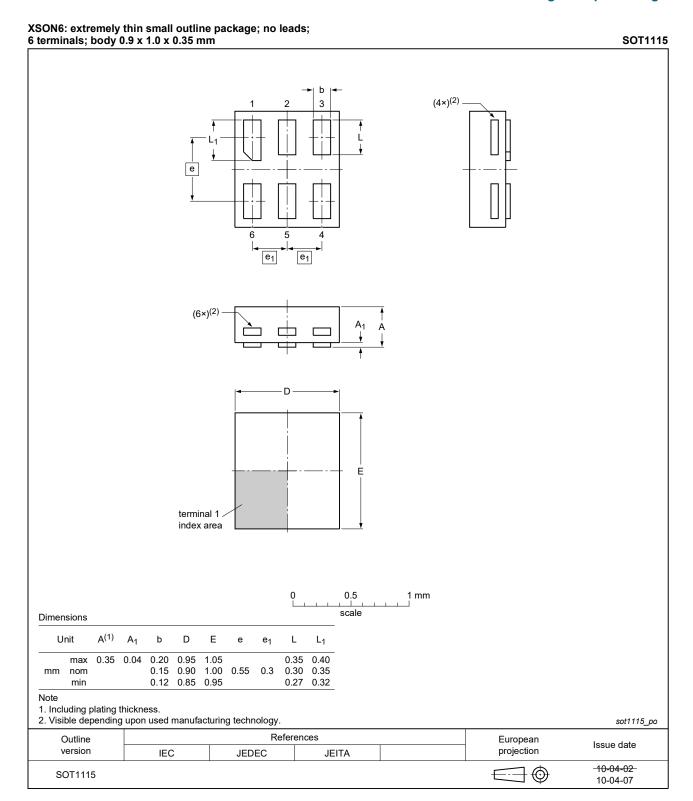


Fig. 9. Package outline SOT1115 (XSON6)

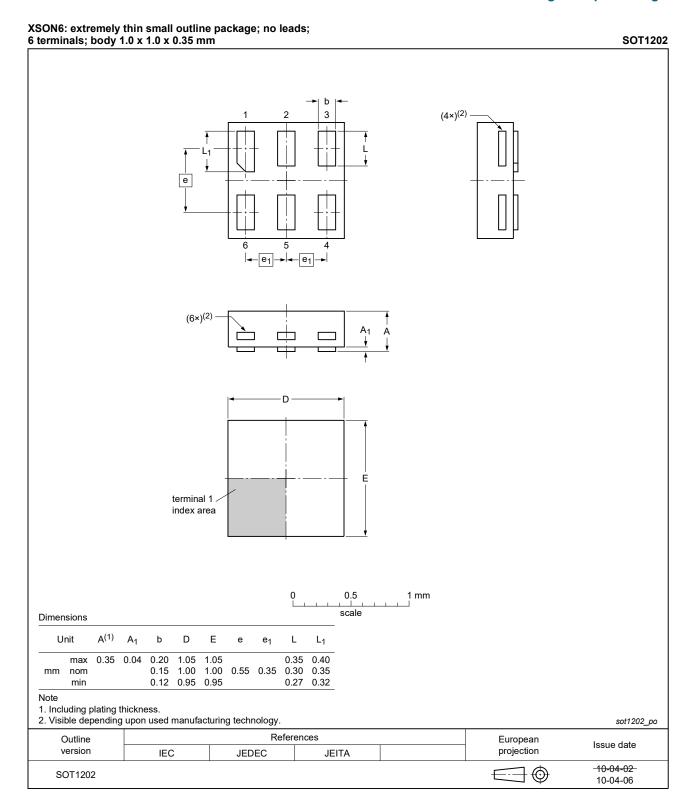


Fig. 10. Package outline SOT1202 (XSON6)

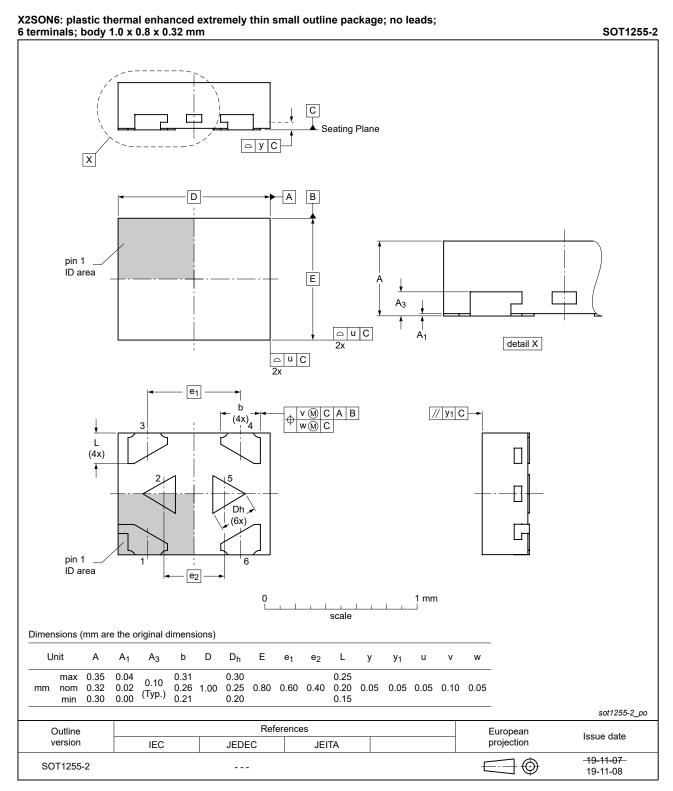


Fig. 11. Package outline SOT1255-2 (X2SON6)

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### 13. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

## 14. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
74LVC1G11 v.13	20230509	Product data sheet	-	74LVC1G11 v.12				
Modifications:	Package S	OT1255 (X2SON6) chan	ged to SOT1255-2 (	X2SON6).				
74LVC1G11 v.12	20220204	Product data sheet	74LVC1G11 v.11					
Modifications:	• SOT363 (S	SOT363 (SC-88) package changed to SOT363-2 (TSSOP6) package.						
74LVC1G11 v.11	20210921	Product data sheet	-	74LVC1G11 v.10				
Modifications:	<ul><li>Type numb</li><li><u>Table 5</u>: De</li></ul>	Type number 74LVC1G11GF (SOT891/XSON6) removed.						
74LVC1G11 v.10	20170719	Product data sheet	-	74LVC1G11 v.9				
Modifications:	guidelines	of this data sheet has be of Nexperia. have been adapted to th	· ·	. ,				
74LVC1G11 v.9	20161128	Product data sheet	-	74LVC1G11 v.8				
Modifications:	• <u>Table 7</u> : Th	e maximum limits for lea	kage current and su	pply current have changed.				
74LVC1G11 v.8	20150917	Product data sheet	-	74LVC1G11 v.7				
Modifications:	Added type	number 74LVC1G11GX	(SOT1255/X2SON	6).				
74LVC1G11 v.7	20120704	Product data sheet	-	74LVC1G11 v.6				
Modifications:	Package out	utline drawing of SOT886	6 (Fig. 8) modified.					
74LVC1G11 v.6	20111209	Product data sheet	-	74LVC1G11 v.5				
Modifications:	Legal page	s updated.						
74LVC1G11 v.5	20100730	Product data sheet	-	74LVC1G11 v.4				
74LVC1G11 v.4	20070801	Product data sheet	-	74LVC1G11 v.3				
74LVC1G11 v.3	20060906	Product data sheet	-	74LVC1G11 v.2				
74LVC1G11 v.2	20050503	Product data sheet	-	74LVC1G11 v.1				
74LVC1G11 v.1	20041130	Product data sheet	-	-				

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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#### Single 3-input AND gate

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