

Low voltage fast-switching NPN power transistor

Features

- Very low collector-emitter saturation voltage
- High current gain characteristic
- Fast switching speed
- Miniature SOT-23 plastic package for surface mounting circuits

Description

The device in a NPN transistor manufactured using new "PB-HCD" (Power Bipolar High Current Density) technology. The resulting transistor shows exceptional high gain performances coupled with very low saturation voltage.

The complementary PNP is the 2STR2160.

Applications

- LED
- Battery charger
- Motor and relay driver
- Voltage regulation

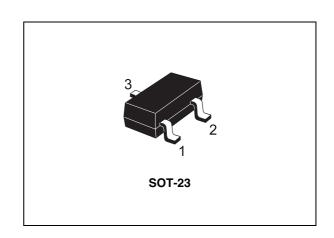


Figure 1. Internal schematic diagram

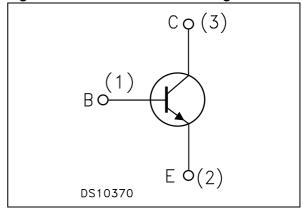


Table 1. Device summary

Order code	Marking	Package	Packing
2STR1160	160	SOT-23	Tape and reel

Content 2STR1160

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2STR1160 Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-base voltage (I _E = 0)	60	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	60	V
V _{EBO}	Emitter-base voltage (I _C = 0)	5	V
I _C	Collector current	1	Α
I _{CM}	Collector peak current (t _P < 5ms)	2	Α
P _{tot}	Total dissipation at T _{amb} = 25°C	0.5	W
T _{stg}	Storage temperature	-65 to 150	°C
T _J	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-amb} ⁽¹⁾	Thermal resistance junction-amb max	250	°C/W

^{1.} Device mounted on PCB area of 1 cm²

Electrical characteristics 2STR1160

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E =0)	V _{CB} = 60 V			0.1	μА
I _{EBO}	Emitter cut-off current (I _C =0)	$V_{EP} = 5 \text{ V}$			0.1	μА
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	Ι _C = 100 μΑ	60			V
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = 10 mA	60			V
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E = 100 μA	5			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$I_C = 0.5 \text{ A}$ $I_B = 50 \text{ mA}$ $I_C = 1 \text{ A}$ $I_B = 100 \text{ mA}$		130 210	210 430	mV mV
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 1 A I _B = 100 mA		0.9	1.25	V
h _{FE} ⁽¹⁾	DC current gain	$\begin{split} I_{C} &= 0.5 \text{ A} & V_{CE} &= 2V \\ I_{C} &= 1 \text{ A} & V_{CE} &= 2V \\ I_{C} &= 2 \text{ A} & V_{CE} &= 2V \end{split}$	85	250 130 30	560	
t _{on} t _{off}	Resistive load Turn-on time Turn-off time	$I_C = 1.5 \text{ A}$ $V_{CC} = 10 \text{ V}$ $I_{B1} = -I_{B2} = 150 \text{ mA}$ $V_{BB(off)} = -5 \text{ V}$		220 500		ns ns

^{1.} Pulsed duration = 300 μ s, duty cycle $\leq .5\%$

2.1 Typical characteristic (curves)

Figure 2. DC current gain (@ V_{CE}=1 V) Figure 3. DC current gain (@ V_{CE}=2 V)

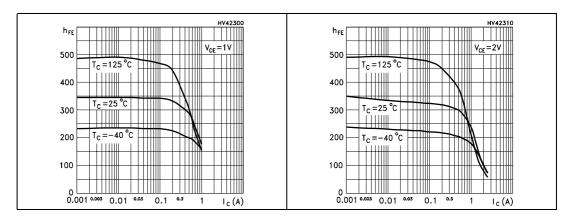


Figure 4. Base-emitter on voltage

Figure 5. Base-emitter saturation voltage

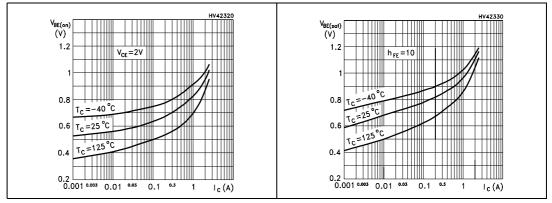
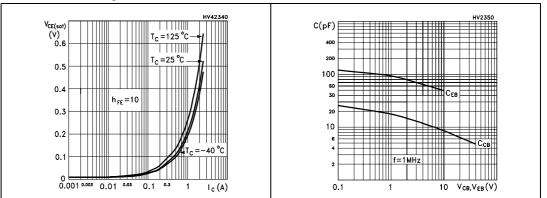


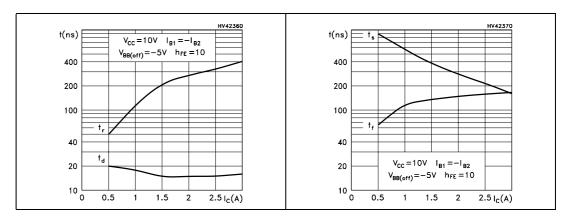
Figure 6. Collector-emitter saturation voltage

Figure 7. Capacitance curves



Electrical characteristics 2STR1160

Figure 8. Resistive load switching time Figure 9. Resistive load switching time



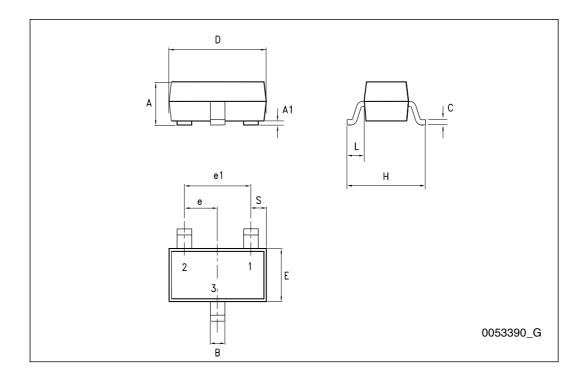
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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SOT-23 mechanical data

DIM.	mm.				
	min.	typ	max.		
А	0.89		1.4		
A1	0		0.1		
В	0.3		0.51		
С	0.085		0.18		
D	2.75		3.04		
е	0.85		1.05		
e1	1.7		2.1		
E	1.2		1.6		
Н	2.1		2.75		
L		0.6			
S	0.35		0.65		



0.9 1.9 1.45 1.45 mm

Figure 10. SOT-23 footprint

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Revision history 2STR1160

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
12-Feb-2008	1	Initial release

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