TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT PROCESS)

# 2SA1931

### **High-Current Switching Applications**

Unit: mm

- Low saturation voltage:  $V_{CE}$  (sat) = -0.4 V (max)
- High-speed switching time:  $t_{stg} = 1.0 \mu s$  (typ.)
- Complementary to 2SC4881

#### **Absolute Maximum Ratings (Tc = 25°C)**

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	-60	V	
Collector-emitter voltage		V <sub>CEO</sub>	-50	٧	
Emitter-base voltage		V <sub>EBO</sub>	-7	V	
Collector current		I <sub>C</sub>	-5	Α	
Base current		Ι <sub>Β</sub>	-1	А	
Collector power dissipation	Ta = 25°C	Pc	2.0	W	
	Tc = 25°C	FC FC	20		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

BASE COLLECTOR **EMITTER JEDEC** JEITA SC-67 TOSHIBA 2-10R1A

Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

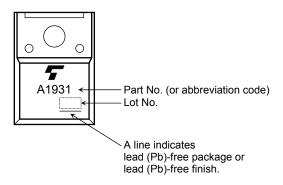
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

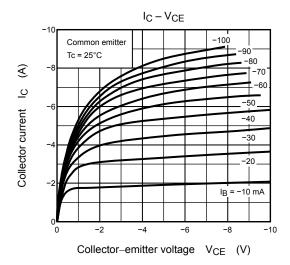
## Electrical Characteristics (Tc = 25°C)

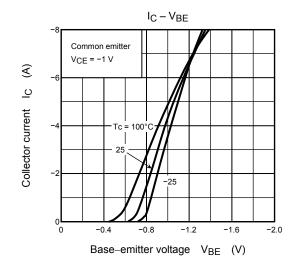
Characteristic		Symbol	Test Conditions	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0	_	_	-1	μΑ
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = -7 V, I <sub>C</sub> = 0	-	_	-1	μΑ
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-50	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = -1 V, I <sub>C</sub> = 1 A	100	_	300	
		h <sub>FE (2)</sub>	V <sub>CE</sub> = -1 V, I <sub>C</sub> = -3 A	60	_	_	
Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = -2 A, I <sub>B</sub> = -0.2 A	-	-0.2	-0.4	V
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = -2 A, I <sub>B</sub> = -0.2 A	_	-0.9	-1.5	V
Transition frequency		f <sub>T</sub>	V <sub>CB</sub> = -1 V, I <sub>C</sub> = -1 A	_	60	_	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1 MHz	_	100	_	pF
Switching time Storag	Turn-on time	t <sub>on</sub>	Output $ \begin{array}{c c}  & & & & & & & & & \\  & & & & & & & \\  & & & &$	_	0.1	_	
	Storage time	t <sub>stg</sub>		_	1.0	_	μs
	Fall time	t <sub>f</sub>		_	0.1	_	

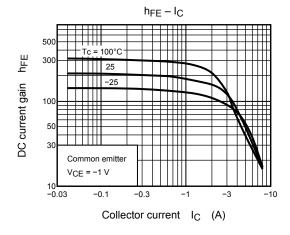
## Marking

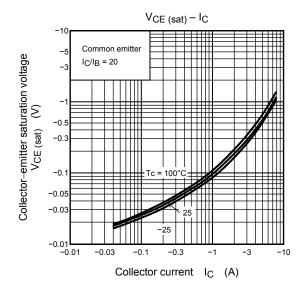


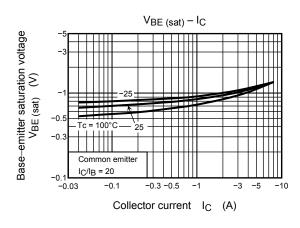
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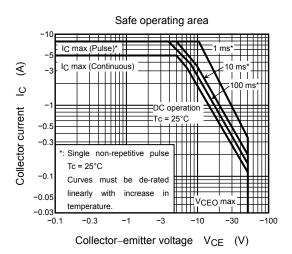












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