Plastic Darlington Complementary Silicon Power Transistors

Plastic Darlington complementary silicon power transistors are designed for general purpose amplifier and low-speed switching applications.

Features

- ESD Ratings: Machine Model, C; > 400 V
 - Human Body Model, 3B; > 8000 V
- Epoxy Meets UL 94 V-0 @ 0.125 in
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Deting	Cumh cl	Value	11:0:4
Rating	Symbol	Value	Unit
Collector–Emitter Voltage 2N6034G 2N6035G, 2N6038G 2N6036G, 2N6039G	V _{CEO}	40 60 80	Vdc
Collector–Base Voltage 2N6034G 2N6035G, 2N6038G 2N6036G, 2N6039G	V _{CBO}	40 60 80	Vdc
Emitter-Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	4.0	Adc
Collector Current – Peak	I _{CM}	8.0	Apk
Base Current	Ι _Β	100	mAdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	40 320	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.12	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	83.3	°C/W

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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4.0 AMPERES DARLINGTON COMPLEMENTARY SILICON POWER TRANSISTORS 40, 60, 80 VOLTS, 40 WATTS



See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
$\begin{array}{l} \mbox{Collector-Emitter Sustaining Voltage} \\ (I_C = 100 \mbox{ mAdc, } I_B = 0) \\ 2N6034G \\ 2N6035G, 2N6038G \\ 2N6036G, 2N6039G \end{array}$	V _{CEO(sus)}	40 60 80	- - -	Vdc
Collector-Cutoff Current	I _{CEO}			μΑ
(V _{CE} = 40 Vdc, I _B = 0) 2N6034G		-	100	
(V _{CE} = 60 Vdc, I _B = 0) 2N6035G, 2N6038G		_	100	
(V _{CE} = 80 Vdc, I _B = 0) 2N6036G, 2N6039G		_	100	
Collector-Cutoff Current	I _{CEX}			μΑ
(V _{CE} = 40 Vdc, V _{BE(off)} = 1.5 Vdc) 2N6034G		_	100	
(V _{CE} = 60 Vdc, V _{BE(off)} = 1.5 Vdc) 2N6035G, 2N6038G		_	100	
(V _{CE} = 80 Vdc, V _{BE(off)} = 1.5 Vdc) 2N6036G, 2N6039G		_	100	
(V _{CE} = 40 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 125°C) 2N6034G		_	500	
(V _{CE} = 60 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 125°C) 2N6035G, 2N6038G		_	500	
(V _{CE} = 80 Vdc, V _{BE(off)} = 1.5 Vdc, T _C = 125°C) 2N6036G, 2N6039G		_	500	
Collector–Cutoff Current	I _{CBO}			mAdc
(V _{CB} = 40 Vdc, I _E = 0) 2N6034G		_	0.5	
(V _{CB} = 60 Vdc, I _E = 0) 2N6035G, 2N6038G		_	0.5	
(V _{CB} = 80 Vdc, I _E = 0) 2N6036G, 2N6039G		_	0.5	
Emitter–Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_{C} = 0$)	I _{EBO}	_	2.0	mAdc
ON CHARACTERISTICS				
DC Current Gain ($I_C = 0.5 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}$) ($I_C = 2.0 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}$) ($I_C = 4.0 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}$)	h _{FE}	500 750 100	- 15,000 -	_
Collector–Emitter Saturation Voltage ($I_C = 2.0 \text{ Adc}, I_B = 8.0 \text{ mAdc}$) ($I_C = 4.0 \text{ Adc}, I_B = 40 \text{ mAdc}$)	V _{CE(sat)}	- -	2.0 3.0	Vdc
Base–Emitter Saturation Voltage (I _C = 4.0 Adc, I _B = 40 mAdc)	V _{BE(sat)}	-	4.0	Vdc
Base–Emitter On Voltage (I _C = 2.0 Adc, V _{CE} = 3.0 Vdc)	V _{BE(on)}	-	2.8	Vdc
DYNAMIC CHARACTERISTICS				
Small–Signal Current–Gain (I _C = 0.75 Adc, V _{CE} = 10 Vdc, f = 1.0 MHz)	h _{fe}	25	-	_
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz) 2N6034G, 2N6035G, 2N6036G 2N6038G, 2N6039G	C _{ob}	_	200 100	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. *Indicates JEDEC Registered Data.



Figure 1. Switching Times Test Circuit







Figure 3. Thermal Response



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 4 and 5 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)}$ $< 150^{\circ}$ C. T_{J(pk)} may be calculated from the data in Figure 3. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



Cib

2.0

V_R, REVERSE VOLTAGE (VOLTS) Figure 6. Capacitance

4.0 6.0

10

20

40



30

20

10

0.04 0.06 0.1

PNP NPN

0.2

0.4 0.6 1.0

ACTIVE-REGION SAFE-OPERATING AREA

Figure 7. DC Current Gain









ORDERING INFORMATION

Device	Package	Shipping
2N6034G	TO-225 (Pb-Free)	500 Units / Box
2N6035G	TO-225 (Pb-Free)	500 Units / Box
2N6036G	TO-225 (Pb-Free)	500 Units / Box
2N6038G	TO-225 (Pb-Free)	500 Units / Box
2N6039G	TO-225 (Pb-Free)	500 Units / Box

PACKAGE DIMENSIONS



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